REDUWAN

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TECHNICAL SKILLS

Languages:	Java, Python, TypeScript, Golang, Rust, Bash
Framework:	Spring Boot, Spring MVC, Spring Cloud, Spring AI, JPA/Hibernate, Angular, Nextjs, Nodejsjs, Nestjs, FastAPI
Databases:	MySQL, PostgreSQL, MongoDB, Redis
Cloud:	AWS (EC2, ECS, S3, Lambda, RDS, IAM, VPC, ALB, Bedrock, Fargate), Google (Cloud, Cloud Functions, Pub/Sub)
DevOps:	Docker, Kubernetes, Helm, Linux, CI/CD (GitHub Actions, Jenkins), NGINX
Tools:	Kibana, Grafana, Prometheus, Seq, Loki, Git, Maven, JUnit, Mockito, SonarQube, RabbitMQ, Kafka, Spring Batch, SLF4J/Logback, Sentry, Spring Cloud Gateway, MapStruct, Swagger/OpenAPI
AI Engineering:	Claude AI, LangChain, LangGraph, LlamaIndex, Pytorch, vLLM, pgvector, Hugging Face

WORK EXPERIENCE

Microland Microsoltuions Inc. March 2023 – August 2025
Backend & DevOps Engineer (Contract) Remote (USA)

- Collaborated with backend engineers to build event-driven microservices for an enterprise ERP platform using Go (Golang), Spring Boot, Apache Kafka, and REST APIs, handling 20K daily updates with sub-200ms API response times.
- Implemented asynchronous event processing and high-throughput background workers with Go and Kafka across core backend services, replacing synchronous calls and reducing peak order-processing latency by 30% (900ms to 620ms).
- Migrated legacy Spring MVC monolithic modules to containerized Go and Spring Boot microservices behind a Spring Cloud Gateway, boosting deployment frequency from bi-weekly to 2–3 times per week.
- Optimized JPA/Hibernate queries and PostgreSQL indexing for high-volume inventory, financial ledger, and HR reporting, reducing critical dashboard query execution times by 70% (5s to 1.5s).
- Developed REST APIs and integrated Spring Batch background job processing with Angular dashboards to automate nightly batch jobs, reducing daily manual inventory reconciliation time from 40 mins/day to 30 mins/day.
- Migrated legacy on-premise services to AWS (ECS Fargate) using Docker, custom Go deployment tooling, and GitHub Actions CI/CD, automating deployments to 15 mins from 2 hours manual and scaling capacity from 7K to 20K daily updates with zero downtime.
- Introduced centralized structured logging (SLF4J/Logback), error tracking (Sentry), and metrics (Prometheus/Grafana) across core services, cutting average production incident investigation time (MTTR) by 40% (from 50 mins to 30 mins).
- Maintained high code quality and reliable delivery cadences in Agile/Scrum sprints through peer code reviews, SonarQube static analysis, JUnit/Mockito testing, and cross-functional collaboration across backend, frontend, and QA teams.

Instapay Technologies Sdn Bhd March 2022 – January 2023
Backend Engineer Kuala Lumpur, Malaysia

- Engineered secure RESTful Web APIs using Java and Spring Boot, implementing JWT authentication, custom middleware, and Role-Based Access Control (RBAC) to enforce security policies across 5+ internal user roles.
- Worked on asynchronous backend workflows and Web API endpoints utilizing CompletableFuture patterns and Spring Events, improving API responsiveness and token management for core financial account operations.
- Designed multi-step data processing pipelines, enforcing strict request validation with Jakarta Bean Validation, state transition management, and DTO mapping to support seamless client onboarding via Angular Reactive Forms.
- Collaborated on full-stack data flow and state management between Spring Boot backend services and Angular web portals, optimizing JSON payloads and reducing portal response times for 50+ concurrent staff users.
- Streamlined API integration and cross-service debugging using Spring Boot logging (SLF4J/Logback), Swagger/OpenAPI specifications, and Postman to isolate network bottlenecks and ensure system reliability.

VLSI Mems & Research July 2021 – December 2021
R&D Engineer Kuala Lumpur, Malaysia

- Designed end-to-end low-level system parsing JSON configuration files with singleton design pattern, reducing validation time by 25% and streamlining customer design parameter processing.
- Automated IEEE 1687 standard IP Design Compiler using validation fault detection in chip testing workflows.
- Developed C++ API integrating two distinct IP components into unified Compression Diagnose TPL logic flow, improving code reusability through optimization and reducing compilation overhead.

EDUCATION

University of Debrecen, Hungary Sep 2025 – Sep 2027
Master of Science in Computer Science (Artificial Intelligence)

International Islamic University (UIA), Malaysia Feb 2018 – Feb 2022
Bachelor of Engineering (Electrical & Computer Engineering)